

10/8/3, 084

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- #5 nakamura<in>au <and>pipeline<in>ti
- #6 benini<in>au<and>interfaces<in>ti
- #7 benini<in>au<and>interfaces<in>ti
- #8 (bus<and>(interface\*<or>conductor\*)<and>delay<IN>metadata)
- #9 (bus<and>(interface\*<or>conductor\*)<and>delay<IN>metadata)
- #10 (bus<near/1>invert\* <and> delay<near/1>circuit<IN>metadata)
- #11 (bus<and>(interface\*<or>conductor\*)<and>delay<IN>metadata)
- #12 (bus<near/1>invert <and> (delay<near/1>circuit)<IN>metadata)
- #13 (bus<and>(interface\*<or>conductor\*)<and>delay<IN>metadata)
- #14 (bus<near/1>invert <and> delay<IN>metadata)
- #15 (bus<near/1>invert <and> delay<IN>metadata)
- #16 (bus<near/1>invert <and> delay<IN>metadata)
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